

I'm not a bot































PFC rating Active PFC is the use of power electronics to change the waveform of current drawn by a load to match the power factor.[25] Some types of the active PFC are buck, boost, buck-boost and synchronous converter. Active power factor correction can be single-stage or multi-stage. In the case of a switched-mode power supply, a boost converter is inserted between the bridge rectifier and the main input capacitors. The boost converter attempts to maintain a constant voltage at its output while drawing a current that is always in phase with and at the same frequency as the line voltage. Another switched-mode converter inside the power supply produces the desired output voltage from the DC bus. This approach requires additional semiconductor switches and control electronics but permits cheaper and smaller passive components. It is frequently used in practice. For a three-phase SMPS, the Vienna rectifier configuration may be used to substantially improve the power factor. SMPSs with passive PFC can achieve power factor of about 0.7–0.75. SMPSs with active PFC, up to 0.99 power factor, while a SMPS without any power factor correction have a power factor of only about 0.55–0.65.[26] Due to their very wide input voltage range, many power supplies with active PFC can automatically adjust to operate on AC power from about 100 V (Japan) to 240 V (Europe). That feature is particularly welcome in power supplies for laptops. Dynamic power factor correction (DPFC), sometimes referred to as real-time power factor correction, is used for electrical stabilization in cases of rapid load changes (e.g. at large manufacturing sites). DPFC is useful when standard power factor correction would cause over or under correction.[27] DPFC uses semiconductor switches, typically thyristors, to quickly connect and disconnect capacitors or inductors to improve power factor. 75 MVar capacitor bank in a 150 kV substation Capacitor bank on a utility pole in San Jose, CA, USA Power factors below 1.0 require a utility to generate more than the minimum volt-amperes necessary to supply the real power (watts). This increases generation and transmission costs. For example, if the load power factor were as low as 0.7, the apparent power would be 1.4 times the real power used by the load. Line current in the circuit would also be 1.4 times the current required at 1.0 power factor, so the losses in the circuit would be doubled (since they are proportional to the square of the current). Alternatively, all components of the system such as generators, conductors, transformers, and switchgear would be increased in size (and cost) to carry the extra current. When the power factor is close to unity, for the same kVA rating of the transformer more load current can be supplied.[28] Utilities typically charge additional costs to commercial customers who have a power factor below some limit, which is typically 0.9 to 0.95. Engineers are often interested in the power factor of a load as one of the factors that affect the efficiency of power transmission. With the rising cost of energy and concerns over the efficient delivery of power, active PFC has become more common in consumer electronics.[29] Current Energy Star guidelines for computers[30] call for a power factor of ≥ 0.9 at 100% of rated output in the PC's power supply. According to a white paper authored by Intel and the U.S. Environmental Protection Agency, PCs with internal power supplies will require the use of active power factor correction to meet the ENERGY STAR 5.0 Program Requirements for Computers.[31] In Europe, EN 61000-3-2 requires power factor correction be incorporated into consumer products. Small customers, such as households, are not usually charged for reactive power and so power factor metering equipment for such customers will not be installed. The power factor in a single-phase circuit (or balanced three-phase circuit) can be measured with the wattmeter-ammeter-voltmeter method, where the power in watts is divided by the product of measured voltage and current. The power factor of a balanced polyphase circuit is the same as that of any phase. The power factor of an unbalanced polyphase circuit is not uniquely defined. A direct reading power factor meter can be made with a moving coil meter of the electrodynamic type, carrying two perpendicular coils on the moving part of the instrument. The field of the instrument is energized by the circuit current flow. The two moving coils, A and B, are connected in parallel with the circuit load. One coil, A, will be connected through a resistor and the second coil, B, through an inductor, so that the current in coil B is delayed with respect to current in A. At unity power factor, the current in A is in phase with the circuit current, and coil A provides maximum torque, driving the instrument pointer toward the 1.0 mark on the scale. At zero power factor, the current in coil B is in phase with circuit current, and coil B provides torque to drive the pointer towards 0. At intermediate values of power factor, the torques provided by the two coils add and the pointer takes up intermediate positions.[32] Another electromechanical instrument is the polarized-vane type.[33] In this instrument a stationary field coil produces a rotating magnetic field, just like a polyphase motor. The field coils are connected either directly to polyphase voltage sources or to a phase-shifting reactor if a single-phase application. A second stationary field coil, perpendicular to the voltage coils, carries a current proportional to current in one phase of the circuit. The moving system of the instrument consists of two vanes that are magnetized by the current coil. In operation, the moving vanes take up a physical angle equivalent to the electrical angle between the voltage source and the current source. This type of instrument can be made to register for currents in both directions, giving a four-quadrant display of power factor or phase angle. Digital instruments exist that directly measure the time lag between voltage and current waveforms. Low-cost instruments of this type measure the peak of the waveforms. More sophisticated versions measure the peak of the fundamental harmonic only, thus giving a more accurate reading for phase angle on distorted waveforms. Calculating power factor from voltage and current phases is only accurate if both waveforms are sinusoidal.[34] Power Quality Analyzers, often referred to as Power Analyzers, make a digital recording of the voltage and current waveform (typically either one phase or three phase) and accurately calculate true power (watts), apparent power (VA) power factor, AC voltage, AC current, DC voltage, DC current, frequency, IEC61000-3-2/3-12 Harmonic measurement, IEC61000-3-3/3-11 flicker measurement, individual phase voltages in delta applications where there is no neutral line, total harmonic distortion, phase and amplitude of individual voltage or current harmonics, etc.[35][36] Anglophone power engineering students are advised to remember: ELI the ICE man or ELI on ICE – the voltage E, leads the current I, in an inductor L. The current I leads the voltage E in a capacitor C. Another common mnemonic is CIVIL – in a capacitor (C) the current (I) leads voltage (V), voltage (V) leads current (I) in an inductor (L). ^ Das, J. C. (2015). Power System Harmonics and Passive Filter Design. Wiley, IEEE Press. p. 2. ISBN 978-1-118-86162-2. To distinguish between linear and nonlinear loads, we may say that linear time-invariant loads are characterized so that an application of a sinusoidal voltage results in a sinusoidal flow of current. ^ Boylestad, Robert (2002-03-04). Introductory Circuit Analysis (10th ed.). Prentice Hall. p. 857. ISBN 978-0-13-097417-4. ^ "SI Units – Electricity and Magnetism". CH: International Electrotechnical Commission. Archived from the original on 2007-12-11. Retrieved 14 June 2013. ^ The International System of Units (SI) [SI brochure] (PDF). § 5.3.2 (p. 132, 40 in the PDF file). BIPM. 2006. Archived (PDF) from the original on 2022-03-19. {{cite book}}: CS1 maint: location (link) ^ Authoritative Dictionary of Standards Terms (7th ed.). IEEE. 2000. ISBN 978-0-7381-2601-2. Std. 100 ^ Trial-Use Standard Definitions for the Measurement of Electric Power Quantities Under Sinusoidal, Nonsinusoidal, Balanced, or Unbalanced Conditions. IEEE. 2000. ISBN 978-0-7381-1963-2. Std. 1459-2000. Note 1, section 3.1.1.1, when defining the quantities for power factor, asserts that real power only flows to the load and can never be negative. As of 2013, one of the authors acknowledged that this note was incorrect, and is being revised for the next edition. See Archived 2016-03-04 at the Wayback Machine ^ Suresh Kumar, K. S. (2013). Electric Circuit Analysis. Pearson. p. 8.10. ISBN 978-8-13-179155-4. ^ Duddell, W. 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(2004), "Performance of Grid-Connected Induction Generator under Naturally Commutated AC Voltage Controller", Electric Power Components and Systems, 32 (7): 691–700, doi:10.1080/15325000490461064, S2CID 110279940, Accordingly, the generator will consume active power from the grid, which leads to negative power factor. ^ Ewald Fuchs; Mohammad A. S. Masoum (14 July 2015). Power Quality in Power Systems and Electrical Machines. Elsevier Science. pp. 432–. ISBN 978-0-12-800988-8. The DPF is the cosine of the angle between these two quantities ^ Sankaran, C. (1999), Effects of Harmonics on Power Systems, Electro-Test, ...and voltage-time relationship deviates from the pure sine function. The distortion at the point of generation is very small (about 1% to 2%), but nonetheless it exists. ^ "Single-phase load harmonics vs. three-phase load harmonics" (PDF), Power System Harmonics, Pacific Gas and Electric, archived from the original (PDF) on 2015-09-24, retrieved 2013-11-26 ^ "Harmonic Effects" (PDF), Harmonics and IEEE 519, CA: EnergyLogix Solutions ^ Sankaran, C. (1999), "Transformers", Effects of Harmonics on Power Systems, Electro-Test ^ Sankaran, C. (1999), "Motors", Effects of Harmonics on Power Systems, Electro-Test, The interaction between the positive and negative sequence magnetic fields and currents produces torsional oscillations of the motor shaft. These oscillations result in shaft vibrations. ^ Schramm, Ben (Fall 2006), "Power Supply Design Principles: Techniques and Solutions, Part 3", Newsletter, Nuvation, archived from the original on 2007-03-09 ^ Wölffe, W.H.; Hurley, W.G. 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[...] There is a very small effect from passive PFC - the power factor grows only from 0.65 to 0.7-0.75. ^ The Active PFC Market is Expected to Grow at an Annually Rate of 12.3% Till 2011, Find articles, Mar 16, 2006, archived from the original on September 1, 2009, Higher-powered products are also likely to use active PFC, since it would be the most cost effective way to bring products into compliance with the EN standard. ^ Power Factor Correction, TECHarp, Passive PFC [...] the power factor is low at 60–80%. [...] Active PFC ... a power factor of up to 95% ^ Why we need PFC in PSU, Silverstone Technology, archived from the original on 2008-12-22, Normally, the power factor value of electronic device without power factor correction is approximately 0.5. [...] Passive PFC [...] 70~80% [...] Active PFC [...] 90~99.9% ^ Brooks, Tom (Mar 2004), "PFC options for power supplies", Taiyo, Electronic products, archived from the original on 2008-12-02, The disadvantages of passive PFC techniques are that they typically yield a power factor of only 0.60 to 0.70 [...] Dual-stage active PFC technology [yields] a power factor typically greater than 0.98 ^ Power Factor Correction (PFC) Basics (PDF) (application note), Fairchild Semiconductor, 2004, archived from the original (PDF) on 2014-06-11, retrieved 2009-11-29 ^ Sugawara, I.; Suzuki, Y.; Takeuchi, A.; Teshima, T. (19-23 Oct 1997), "Experimental studies on active and passive PFC circuits", INTELEC 97, 19th International Telecommunications Energy Conference, pp. 571–78, doi:10.1109/INTELEC.1997.646051, ISBN 978-0-7803-3996-5, S2CID 109885369 ^ Chavez, C.; Houdek, J. A. "Dynamic Harmonic Mitigation and power factor correction". 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Wayne (1978), Standard Handbook for Electrical Engineers (11 ed.), New York: McGraw-Hill, p. 3-29 paragraph 80, ISBN 978-0-07-020974-9 ^ Manual of Electric Instruments Construction and Operating Principles, Schenectady, New York: General Electric, Meter and Instrument Department, 1949, pp. 66-68, GEF-1087A ^ "The Fundamentals of FFT-Based Signal Analysis and Measurement in LabVIEW and LabWindows/CVI". National Instruments Corporation. Retrieved 6 November 2017. ^ "WT3000E Series Precision Power Analyzers" (PDF), Yokogawa Corporation. Archived from the original (PDF) on 7 November 2017. Retrieved 6 November 2017. ^ "Fluke 1760 Three-Phase Power Quality Recorder" (PDF), Fluke Corporation. Archived (PDF) from the original on 2022-10-09. Retrieved 6 November 2017. Harmonics and how they relate to power factor (PDF), U Texas, archived from the original (PDF) on 2011-09-27, retrieved 2010-09-20. Retrieved from "Zero Power Factor Characteristic (ZPFC) of a generator is a curve of the armature terminal voltage and the field current. The machine is operated with a constantly rated armature current at synchronous speed and zero lagging power factor. The Zero Power Factor Characteristic is also called as Potier Characteristic. For maintaining a very low power factor, the alternator is loaded by means of reactors or by an under excited synchronous motor. The shape of ZPFC is very much like that of the O.C.C. The phasor diagram corresponding to zero power factor lagging is shown below: In the phasor diagram shown above, the terminal voltage V is taken as the reference phasor. At zero power factor lagging, the armature current Ia lags behind V by 90 degrees. IaRa is drawn parallel to Ia and IaXaL perpendicular to Ia. Eg is the generated voltage per phase. The phasor diagram at ZPF lagging with the armature resistance Ra neglected is shown below: Where, Far is the armature reaction MMF. It is in phase with the armature current Ia. Ff is the MMF of the main field winding (field MMF). Fr is the resultant MMF. The field MMF Ff is obtained by subtracting Far from Fr so that From the above phasor diagram, it is seen that the terminal voltage V, the reactance voltage drop IaXaL, and the generated voltage Eg all are in phase. Therefore, V is practically equal to the arithmetical difference between Eg and IaXaL. The three MMF phasor Ff, Fr and Far are in phase. Their magnitudes are related by the equation shown below: The above two equations, i.e. equation (1) and (2) forms the basis for the Potier triangle. If the equation (2) is divided both sides by Tf, it is converted into its equivalent field current form. Here Tf is the effective number of turns per pole on the rotor field. Therefore, From the above equation, the sum of the resultant current and the armature reaction current gives the field current.